Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(array with leads same circuit near board and package same alignment and assembly).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 13:13
L2	15	(placement near head same alignment).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 12:56
L3	0	(placement near head with alignment with circuit with package).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 12:57
L4	0	439/79.ccls. and (array with leads same circuit near board and package same alignment and assembly)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 13:18
L9	0	702/117.ccls. and (array with leads same circuit near board and package same alignment and assembly)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 13:17
L11	6	439/70.ccls. and (array with leads same circuit near board and package same alignment and assembly)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/06 13:18